

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani Art Unit : 1765
Serial No.: 09/596,755 Examiner : Robert Kunemund
Filed : June 15, 2000
Title : MANUFACTURING METHOD OF SEMICONDUCTOR AND
 MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE

Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Applicants call attention to the enclosed documents listed on the attached form PTO-1449.

Kindly accept this Information Disclosure Statement under Rule 97(c)(1).

The rule 17(p) certification fee of \$240 is enclosed.

The documents are in the English language hence no concise explanation is necessary per rule 97(c).

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed Form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

Signature

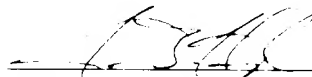
Susan Regan

Type in Stamp (Using of Date on Signing Certificate)

Please apply any charges or credits to Deposit Account
No. 06-1050.

Respectfully submitted,

Date: 1/24/01


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SCH/smr

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